

**CURRENT CLAIMS**

A copy of the claims is provided for the convenience of the Examiner. The claims are not amended.

1. (Previously Presented) A bridge comprising:  
  
a plurality of interface registers that are configured to facilitate communication of data with a plurality of function units, and  
  
a plurality of register transfer units, operably coupled to the plurality of interface registers, that facilitate transfers of data among at least two interface registers of the plurality of interface registers.
  
2. (Previously Presented) The bridge of claim 1, further comprising:  
  
an instruction memory that is configured to contain register transfer instructions, and  
  
wherein the operable coupling of the plurality of register transfer units and the plurality of function units is effected via the register transfer instructions.
  
3. (Previously Presented) The bridge of claim 1, further comprising:  
  
at least one datapath unit, operably coupled to the plurality of register transfer units, that facilitates a transformation of at least one data item of the data that is transferred among the interface registers.